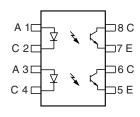


Vishay Semiconductors

## Optocoupler, Phototransistor Output, Dual Channel, SOIC-8 package

## Dual Channel, SOIC-8 package FEATURES



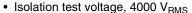


**DESCRIPTION** 

The ILD205T/206T/207T/211T/213T/217T are optically coupled pairs with a Gallium Arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The ILD205T/206T/207T/211T/213T/217T come in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space. In addition to eliminating through-holes requirements, this package conforms to standards for surface mounted devices.

A specified minimum and maximum CTR allows a narrow tolerance in the electrical design of the adjacent circuits. The high  $BV_{CEO}$  of 70 V gives a higher safety margin compared to the industry standard of 30 V.

- · Two channel coupler
- SOIC-8 surface mountable package
- Standard lead spacing of 0.05"
- Available only on tape and reel option (conforms to EIA standard 481-2)



- Compatible with dual wave, vapor phase and IR reflow soldering
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code Y
- CUL file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-2 (VDE 0884) available with option 1

ORDER INFORMATION					
PART	REMARKS				
ILD205T	CTR 40 to 80 %, SOIC-8				
ILD206T	CTR 63 to 125 %, SOIC-8				
ILD207T	CTR 100 to 200 %, SOIC-8				
ILD211T	CTR > 20 %, SOIC-8				
ILD213T	CTR > 100 %, SOIC-8				
ILD217T	CTR > 100 %, SOIC-8				

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ABSOLUTE MAXIMUM RATINGS (1)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT	<u> </u>					
Peak reverse voltage		$V_{R}$	6	V		
Peak pulsed voltage	1 μs, 300 pps		1	Α		
Continuous forward current per channel			30	mA		
Power dissipation		P <sub>diss</sub>	50	mW		
Derate linearly from 25 °C			0.66	mW/°C		
OUTPUT						
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V		
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7	V		
Power dissipation per channel		P <sub>diss</sub>	125	mW		
Derate linearly from 25 °C			1.67	mW/°C		
COUPLER						
Isolation test voltage	t = 1 s	$V_{ISO}$	4000	$V_{RMS}$		
Total package dissipation ambient (2 LEDs and 2 detectors, 2 channels)		P <sub>tot</sub>	300	mW		
Derate linearly from 25 °C			4	mW/°C		
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C		
Operating temperature		T <sub>amb</sub>	- 55 to + 100	°C		
Soldering time from 260 °C (2)		T <sub>sld</sub>	10	S		

#### Notes

Stresses in excess of the absolute Maximum Ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute Maximum Rating for extended periods of the time can adversely affect reliability.

<sup>(2)</sup> Refer to reflow profile for soldering conditions for surface mounted devices.

ELECTRICAL CHARACTERISTCS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>F</sub>		1.2	1.55	V
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>		0.1	100	μΑ
Capacitance	V <sub>R</sub> = 0 V		Co		25		pF
OUTPUT							
Collector emitter breakdown voltage	$I_C = 10 \mu A$		BV <sub>CEO</sub>	70			V
Emitter collector breakdown voltage	$I_E = 10 \mu A$		BV <sub>ECO</sub>	7			V
Collector emitter leakage current	$V_{CE} = 10 \text{ V}, I_F = 0 \text{ A}$		I <sub>CEO</sub>		5	50	nA
Collector emitter capacitance	$V_{CE} = 0 V$		C <sub>CE</sub>		10		pF
COUPLER							
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 2.5 \text{ mA}$		V <sub>CEsat</sub>			0.4	V
Capacitance (input to output)			C <sub>IO</sub>		0.5		pF
Resistance (input to output)			R <sub>IO</sub>		100		GΩ

#### Note

 $T_{amb}$  = 25 °C, unless otherwise specified.

Minimum and maximum values were tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

 $T_{amb} = 25 \, ^{\circ}C$ , unless otherwise specified.



Optocoupler, Phototransistor Output, Vishay Semiconductors Dual Channel, SOIC-8 package

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		ILD205T	CTR <sub>DC</sub>	40		80	%
		ILD206T	CTR <sub>DC</sub>	63		125	%
DC current transfer ratio	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}$	ILD207T	CTR <sub>DC</sub>	100		200	%
		ILD211T	CTR <sub>DC</sub>	20			%
		ILD213T	CTR <sub>DC</sub>	100			%
		ILD205T	CTR <sub>DC</sub>	13	30		%
	\/ - F \/   - 1.0 m \	ILD206T	CTR <sub>DC</sub>	22	45		%
	$V_{CE} = 5 \text{ V}, I_F = 1.0 \text{ mA}$	ILD207T	CTR <sub>DC</sub>	34	70		%
		ILD217T	CTR <sub>DC</sub>	100	120		%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	$I_C = 2 \text{ mA, } R_L = 100 \ \Omega,$ $V_{CC} = 5 \ V$		t <sub>on</sub>	5			μs
Turn-off time	$I_C = 2 \text{ mA, } R_L = 100 \ \Omega, \\ V_{CC} = 5 \ V$		t <sub>off</sub>	4			μs

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification (according to IEC 68 part 1)				55/100/21			
Comparative tracking index		CTI	175		399		
V <sub>IOTM</sub>			6000			V	
V <sub>IORM</sub>			560			V	
P <sub>SO</sub>					350	mW	
I <sub>SI</sub>					150	mA	
T <sub>SI</sub>					165	°C	
Creepage			4			mm	
Clearance			4			mm	
Insulation thickness, reinforced rated	per IEC60950 2.10.5.1		0.2			mm	

#### Note

As per IEC60747-5-2, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

## Vishay Semiconductors

#### Optocoupler, Phototransistor Output, Dual Channel, SOIC-8 package



#### **TYPICAL CHARACTERISTICS**

T<sub>amb</sub> = 25 °C, unless otherwise specified

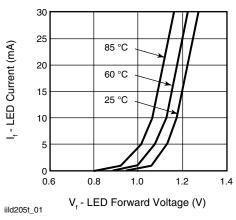


Fig. 1 - Forward Current vs. Forward Voltage

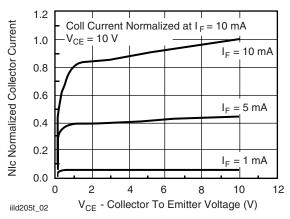


Fig. 2 - Collector Emitter Current vs. V<sub>CE</sub>

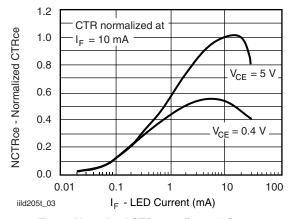


Fig. 3 - Normalized  $\mathsf{CTR}_\mathsf{CE}$  vs. Forward Current

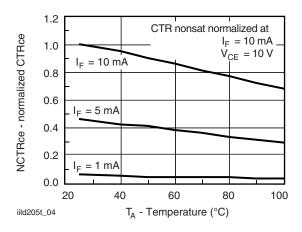


Fig. 4 - Current Transfer Ratio (normalized) vs.
Ambient Temperature

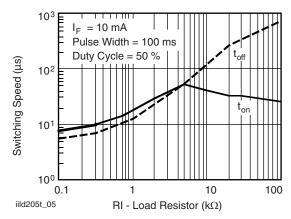


Fig. 5 - Switching Speed vs. Load Resistor

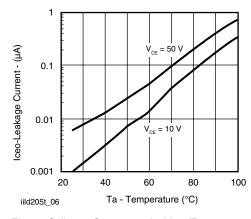


Fig. 6 - Collector Current vs. Ambient Temperature





Optocoupler, Phototransistor Output, Vishay Semiconductors Dual Channel, SOIC-8 package

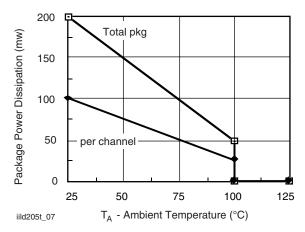
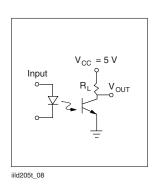


Fig. 7 - Power Dissipation vs. Ambient Temperature



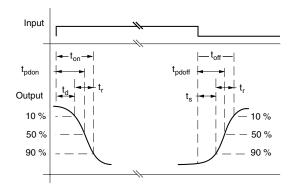


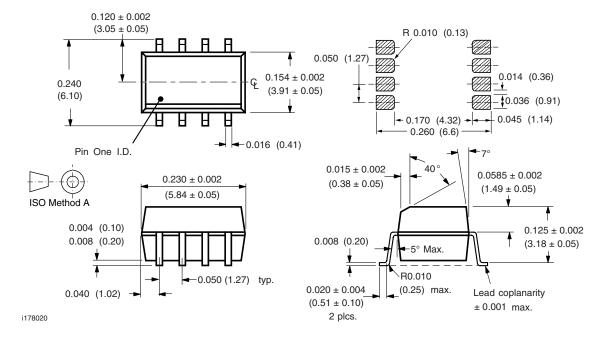
Fig. 8 Switching Test Circuit

Vishay Semiconductors Optocoupler,

Optocoupler, Phototransistor Output, Dual Channel, SOIC-8 package



#### **PACKAGE DIMENSIONS** in inches (millimeters)



# VISHAY.

#### ILD205T/206T/207T/211T/213T/217T

Optocoupler, Phototransistor Output, Vishay Semiconductors Dual Channel, SOIC-8 package

#### **OZONE DEPLETING SUBSTANCES POLICY STATEMENT**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

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Vishay

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