

DATA SHEET

HY86-12LF: 90-Degree Hybrid 0.82-0.90 GHz

Features

- Low cost
- Low profile
- Small SOIC-8 package
- Available on tape and reel
- Available lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020

Description

The HY86-12LF is a 90-degree hybrid tuned for the 0.82–0.90 GHz band. The monolithic circuitry is 100% passive and offers low loss, high isolation and exceptional phase/amplitude balance. It is available in the SOIC-8 surface mount package.



Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.

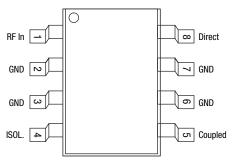
Electrical Specifications at 25 °C

$Z_0 = 50 \Omega$, unless otherwise noted

Parameter	Frequency	Min.	Тур.	Max.	Unit
Frequency		0.82		0.9	GHz
Insertion loss ⁽¹⁾			0.4	0.5	dB
Isolation		25	30		dB
VSWR all ports			1.15:1	1.2:1	
Amplitude balance			±0.5	±0.8	dB
Phase balance			±1	±2	Deg.

1. Less 3 dB power split.

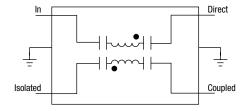
Pin Out



The Pin Out diagram shows the HY86-12LF configured for a Divider/Coupler with Pin 1 as the input port and a 50 Ω termination to be placed at Pin 3. Since the HY86-12LF is symmetric, any nonground pin may be used as the input port. The following table shows the possible pin connection combinations for the HY86-12LF used as a divider/coupler:

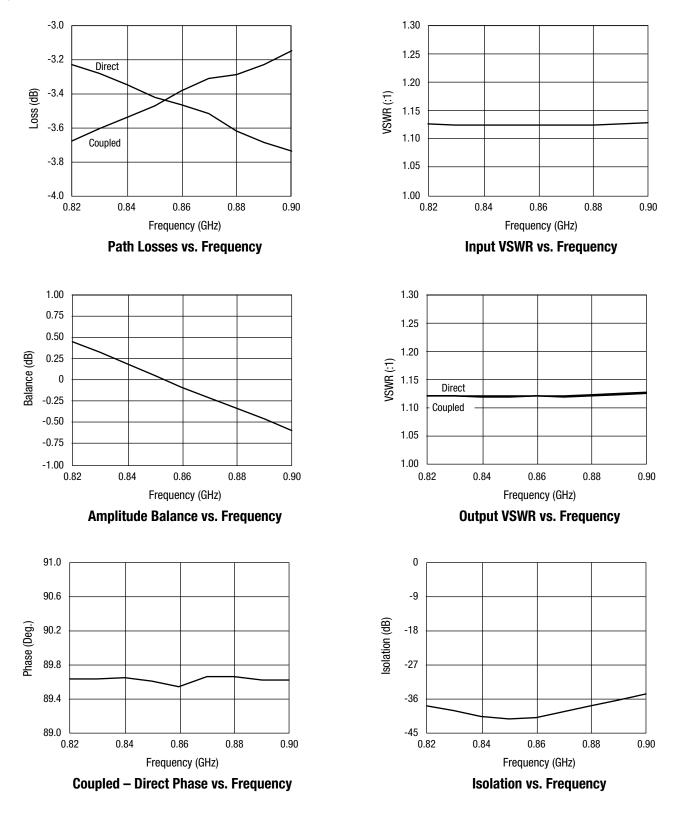
Input	Termination	Direct	Coupled
Pin 1	Pin 4	Pin 8	Pin 5
Pin 4	Pin 1	Pin 5	Pin 8
Pin 5	Pin 8	Pin 4	Pin 1
Pin 8	Pin 5	Pin 1	Pin 4

Block Diagram



Typical Performance Data





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Absolute Maximum Ratings

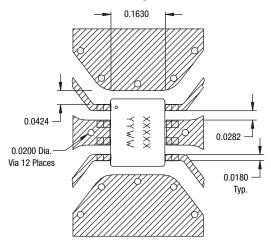
Characteristic	Value
Input power ⁽¹⁾	4 W
Operating temperature	-40 °C to +85 °C
Storage temperature	-65 °C to +150 °C
Electrostatic discharge	125 V

1. Exceeding these parameters may cause irreversible damage.

Performance is guaranteed only under the conditions listed in the specifications table and is not guaranteed under the full range(s) described by the Absolute Maximum specifications. Exceeding any of the absolute maximum/minimum specifications may result in permanent damage to the device and will void the warranty.

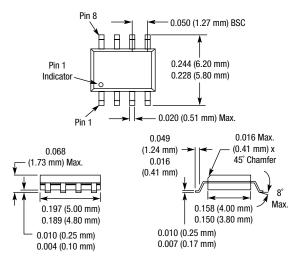
CAUTION: Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

Recommended Board Layout



Materials 10 mil FR-4. Dimensions are in inches.

SOIC-8



Recommended Solder Reflow Profiles

Refer to the "<u>Recommended Solder Reflow Profile</u>" Application Note.

Tape and Reel Information

Refer to the "Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation" Application Note.

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